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INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>				Application Number	10/821,123
				Filing Date	April 8, 2004
				First Named Inventor	Blake W. Little
				Art Unit	2662
				Examiner Name	Kizou, Hassan
Sheet	1	of	1	Attorney Docket Number	65744/P018US/10404217

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
/M.C./	AA	US-5,520,187	May 28, 1996	Snyder	

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ³
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NON PATENT LITERATURE DOCUMENTS					
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc), date, page(s), volume-issue number(s), publisher, city and/or country where published.			T ²
/M.C./	CA	Weste N.H.: "CMOS VLSI DESIGN: a circuits and systems perspective" 1993, Addison/Wesley, page 19, XP002350734			
/M.C./	CB	Anonymous: "Multiplexed Data Acquisition System Considerations" Intersil Application Notes, August 2002, pages 1-3, XP002350732			
/M.C./	CC	Partial International Search Report issued for PCT/US2005/012109 dated November 14, 2005.			

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